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10/586687  
IAP6 Rec'd PCT/PTO 02866 JUL 2006: 2838  
XA-10598  
PATENT APPLICATION

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

First Named Inventor: KUDO, RYOTARO

**Art Unit:**

Intl. Appln. No.: PCT/JP2005/000328

**Examiner:**

Intl. Filing Date: 14 January 2005

**Confirmation No.:**

For: A VOLTAGE CLAMP CIRCUIT, A  
SWITCHING POWER SUPPLY DEVICE, A  
SEMICONDUCTOR INTEGRATED CIRCUIT  
DEVICE, AND A VOLTAGE LEVEL  
CONVERSION CIRCUIT

\* \* \*

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. § 1.56, and without any assertion as to materiality or prior art effect, the documents listed on the attached Form PTO/SB/08A are hereby cited.

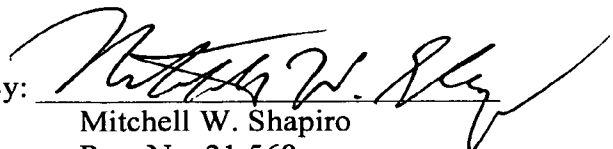
Documents 4 and 5 on the attached List were cited in the specification, on page 1, and their relevance is indicated therein. The other listed documents were cited in the International Search Report (copy attached).

Respectfully submitted,

Date: July 20, 2006

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ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /JZ/



10586687 GAU: 2838  
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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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Complete it Known 10 / 586687

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|----------------------|---------------|
| Application Number   |               |
| Filing Date          | July 20, 2006 |
| First Named Inventor | KUDO, RYOTARO |
| Art Unit             |               |
| Examiner Name        |               |


(use as many sheets as necessary)

|       |   |    |   |                        |          |
|-------|---|----|---|------------------------|----------|
| Sheet | 1 | of | 1 | Attorney Docket Number | XA-10598 |
|-------|---|----|---|------------------------|----------|

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

| Examiner Initials*   | Cite No. <sup>1</sup> | Foreign Patent Document |                     |                                      | Name of Patentee or Applicant of Cited Document | Publication Date MM-DD-YYYY | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | T <sup>6</sup> |
|--|-----------------------|-------------------------|---------------------|--------------------------------------|---|-----------------------------|---|----------------|
|  |                       | Office <sup>3</sup>     | Number <sup>4</sup> | Kind Code <sup>5</sup><br>(if known) |   |                             |   |                |
|  | 4                     | JP                      | 5-327465            | A                                    |   |                             |   | √              |
|  | 5                     | JP                      | 6-69435             | A                                    |   |                             |   | √              |
|  | 6                     | JP                      | 10-163826           |                                      | Fujitsu Ltd.                                    | 06-19-1998                  |   | Abstract       |
|  | 7                     | JP                      | 2003-347921         | A                                    | Renesas Technology                              | 12-05-2003                  | Corresp. to Cite 1  | Abstract       |
|  | 8                     | JP                      | 2000-196433         | A                                    | Sharp Corp.                                     | 07-14-2000                  |   | Abstract       |
|  | 9                     | JP                      | 2000-357949         | A                                    | Texas Instruments                               | 12-26-2000                  | Corresp. to Cite 2  | Abstract       |
|  | 10                    | JP                      | 57-129534           |                                      | NEC Corp.                                       | 08-11-1982                  |   | Abstract       |
|  | 11                    | JP                      | 2003-142999         | A                                    | NEC Electronics                                 | 05-16-2003                  | Corresp. to Cite 3  | Abstract       |

|                       |             |                    |            |
|-----------------------|-------------|--------------------|------------|
| Examiner<br>Signature | /Jue Zhang/ | Date<br>Considered | 04/25/2009 |
|-----------------------|-------------|--------------------|------------|

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.97 and 1.98. This information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. **SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

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